

INTERNATIONAL SEARCH REPORT

PCT/IB2005/050258

A. CLASSIFICATION OF SUBJECT MATTER
 IPC 7 G06F15/78 H04Q7/32 G06F15/80

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F H04Q

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, INSPEC, COMPENDEX

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	WO 02/50700 A (PICOCHIP DESIGNS LIMITED; CLAYDON, ANTHONY, PETER, JOHN) 27 June 2002 (2002-06-27) page 2, lines 2-23 page 5, lines 33-36 page 7, line 20 page 13, line 30 page 15, lines 25-29; figure 1 page 17, lines 6-11; figure 7 page 18, lines 14-16; table 1 ----- -/-	1-6,9-12
A	page 2, lines 2-23 page 5, lines 33-36 page 7, line 20 page 13, line 30 page 15, lines 25-29; figure 1 page 17, lines 6-11; figure 7 page 18, lines 14-16; table 1 ----- -/-	7,8

 Further documents are listed in the continuation of box C. Patent family members are listed in annex.

* Special categories of cited documents :

- *A* document defining the general state of the art which is not considered to be of particular relevance
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- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

- *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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- *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- *&* document member of the same patent family

Date of the actual completion of the international search

20 July 2005

Date of mailing of the international search report

01/08/2005

Name and mailing address of the ISA

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	SOFTWARE DEFINED RADIO FORUM: "Requirements for Radio Software Download for RF Reconfiguration" NN, 'Online' 13 November 2002 (2002-11-13), XP002335657 Retrieved from the Internet: URL: http://www.sdrforum.org/public/approve/d/02_a_0007_v0_00_d1_req_01_22_03.pdf 'retrieved on 2005-07-12! page 23, section 4.2.2 -----	2-6, 10-12
Y	US 6 282 627 B1 (WONG DALE ET AL) 28 August 2001 (2001-08-28) column 6, lines 28-38 -----	1,9
A	US 6 047 115 A (MOHAN ET AL) 4 April 2000 (2000-04-04) column 3, lines 1-13; figure 1 -----	7,8
A	US 2003/105949 A1 (MASTER PAUL L ET AL) 5 June 2003 (2003-06-05) paragraph '0044!; figure 1 paragraph '0048! paragraphs '0059! - '0061! paragraphs '0063!, '0064! paragraphs '0083! - '0086! -----	7,8
Y	ARNDT M ET AL: "SOFTWARE RADIO: THE CHALLENGES FOR RECONFIGURABLE TERMINALS LA RADIO LOGICIELLE: LES ENJEUX POUR DES TERMINAUX RECONFIGURABLES" ANNALES DES TELECOMMUNICATIONS - ANNALS OF TELECOMMUNICATIONS, PRESSES POLYTECHNIQUES ET UNIVERSITAIRES ROMANDES, LAUSANNE, CH, vol. 57, no. 7/8, July 2002 (2002-07), pages 570-612, XP001124779 ISSN: 0003-4347 pages 582-592; figures 6,7,10,13-15 -----	1,9
A	KOREN I ET AL: "A DATA-DRIVEN VLSI ARRAY FOR ARBITRARY ALGORITHMS" COMPUTER, IEEE COMPUTER SOCIETY, LONG BEACH., CA, US, US, vol. 21, no. 10, 1 October 1988 (1988-10-01), pages 30-43, XP000118929 ISSN: 0018-9162 page 33, left-hand column, paragraph 4; figure 4 ----- -/-	1,9

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>YEUNG A K W ET AL: "A reconfigurable data-driven multiprocessor architecture for rapid prototyping of high throughput DSP algorithms" SYSTEM SCIENCES, 1993, PROCEEDING OF THE TWENTY-SIXTH HAWAII INTERNATIONAL CONFERENCE ON WAILEA, HI, USA 5-8 JAN. 1993, LOS ALAMITOS, CA, USA, IEEE, US, vol. i, 5 January 1993 (1993-01-05), pages 169-178, XP010640447 ISBN: 0-8186-3230-5 page 173; figure 4</p> <p>-----</p>	1,9
A	<p>GAY-BELLILE, O. MARCHAL, X. BURNS, G. VAIDYANATHAN, K: "A reconfigureable superimposed 2D-mesh array for channel equalization" IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, 2002. ISCAS 2002, August 2002 (2002-08), pages 893-896, XP002335646 ISBN: 0-7803-7448-7 the whole document</p> <p>-----</p>	1,9

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